1. INTRODUCTION

The last decade has witnessed a major interest in squeeze-mode MR vibration isolators (VI-MRs) (Farjoud et al., 2011; Kim et al., 2008; Zhang et al., 2011). The majority of previous research has been focused mainly on theoretical studies, e.g. Jolly and Carlson (1996) and Stanway et al. (2000). It is worthwhile mentioning that certain solutions for this type of VI-MRs have been patented (Kim, 2012; Sapiński, 2013).

The investigated autonomous control system (ACS) is designed to be used for control of a prototype squeeze-mode MR vibration isolator (VI-MR). The VI-MR is intended for use as an actuating element in a semi-active engine automotive mount system designed to reduce the engine's vibrations due to fluctuating inertia forces (caused by unbalanced element of the crank system), to fluctuations of the drive torque transmitted from the engine onto the drive system components and to the action of some random forces (Kamiński and Pokorski, 1983; Snamina and Sapiński, 2014).

The ACS is based on an ultra low power microcontroller MSP430F5529 (MSP430) (Texas Instruments INC, 2013). Prior to the fabrication of ACS, testing was done on a previously developed power driver dedicated for VI-MR and based on an integrated interface VNH2SP30-E (Rosól and Sapiński, 2014).

The ACS requires a dedicated real-time clock system, to enable the measurement of process variables and to develop the control method with the predetermined sampling period (Philip and Laplante). The guaranteed constant sampling period enables the use of the theory of discrete control systems in developing control algorithms for VI-MR. Besides such approach allows to employ model-based design method in simulation, testing and implementation of regulators on the target hardware platform.

Measurement data from the ACS are monitored and processed using a serial bus Controller Area Network (CAN) (Bosch GmbH, 1991). This bus was selected as it is intended for use in control of the VI-MR to be installed in the engine mount in the vehicle where CAN acts as the primary data transmission interface.

The engineered ACS was tested in the laboratory conditions in two configurations: the open loop and in the feedback loop (with a PID controller) systems. Testing was done under the loading applied through the electromagnet coil whose resistance and induction were similar to the parameters of the control coil in the VI-MR.

2. DESIGN STRUCTURE

The block diagram of the ACS, shown in Fig. 1, comprises the following units: the MSP430 block with analogue and digital signal outputs, the measurement block, the CAN interface, the power-supply and the USB interface.

The key component of the ACS is the MSP430 supporting the measurement and control application. The MSP430 features high program memory and data memory capacity, numerous integrated peripherals (USB, SPI, 12C, UART, an A/D converter, a multiply unit) and the supply voltages can be generated via an USB port. The major technical parameters of the MSP430 are summarised in Tab. 1.

The MSP430, the measurement block and the CAN bus block are supplied with voltages +5 V and +3/3 V from the power supply block. The ACS may be supplied via a micro-USB port (+5 V) or from a power supply unit delivering the voltage in the range (+3,3, +12) V.

The measurement block incorporating the system for conditioning four analogue signals enables the gain and offset control, so the sensors operating in the voltage range ±10 V can be easily connected to the ACS. The voltage range handled by the measurement block necessitates the use of a DC/DC +5 V±15 V converter in the power supply block. The MSP430 has an integrated USB interface which can be utilised for programming and data exchange with the microprocessor. The current levels to be han-
The USB interface block are limited to 100 mA and the data lines D+ and D− are surge-voltage protected.

The CAN bus block enables the ACS communication with peripheral devices via the CAN bus. This block is implemented on an integrated SPI/CAN converter and a CAN transceiver. The maximal data transmission throughput is 1 Mbit/s.

![Fig. 1. Block diagram of the ACS](image)

**Tab. 1. Technical parameters of MSP430**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Program memory</td>
<td>128 kB</td>
</tr>
<tr>
<td>Data memory SRAM</td>
<td>8+2 kB (additional 2 kB without USB)</td>
</tr>
<tr>
<td>I/O number</td>
<td>Max. 63</td>
</tr>
<tr>
<td>Number of Timers/Counters</td>
<td>4</td>
</tr>
<tr>
<td>Watchdog</td>
<td>Yes</td>
</tr>
<tr>
<td>PWM mode</td>
<td>Yes</td>
</tr>
<tr>
<td>Communication protocols (USCI)</td>
<td>Channel A: UART/LIN/IrDA/SPI</td>
</tr>
<tr>
<td></td>
<td>Channel B: PC/SPI</td>
</tr>
<tr>
<td>DMA</td>
<td>3 channels</td>
</tr>
<tr>
<td>Multiply unit (MPY)</td>
<td>32x32</td>
</tr>
<tr>
<td>Comparator</td>
<td>Yes</td>
</tr>
<tr>
<td>Integrated temperature sensor</td>
<td>Yes</td>
</tr>
<tr>
<td>A/D converter</td>
<td>12 channels, 12-bit</td>
</tr>
<tr>
<td>Integrated USB interface</td>
<td>Yes</td>
</tr>
</tbody>
</table>

3. TESTING

3.1. Laboratory setup

The schematic diagram of the laboratory set-up for ACS testing is shown in Fig. 2. The power interface and ACS are supplied from the battery +12 V. The monitoring and data acquisition are supported by a purpose-developed application written in the Lab-View and activated on a PC. The data transmission between the ACS and PC is effected via a sbRIO-9639 card equipped with the CAN interface (ACS connection) and the Ethernet (PC connection). The power interface ensures the current levels 11−12 A to be as required in the electromagnet coil.

The application supporting the operation of measurement sensors and control of the electromagnet coil is MSP430 enabled. The primary function of this application, written in the C/C++ language in the Code Composer Studio (CCS) environment is to generate the command signal basing on the current measured in the electromagnet coil, so that the current should be maintained on the preset level. Besides, the application configures and handles the digital analogue signals from the microcontroller (including the signals from the PWM signal generator, an A/C converter and the SPI interface supporting the communication with the SPI/CAN converter). To lend the controller a deterministic feature, the control algorithm is activated in the procedure of interruption from the timer. The time interval of this interruption is set to be 1 kHz. This value was selected basing on the transmissibility characteristics of the power interface, obtained for the VI-MR, shown in Fig. 3. The gain $M_g$ is derived from formula (1).

$$M_g = 20 \log_{10} \left( \frac{f(f)}{\alpha(f)} \right)$$

![Fig. 2. Block diagram of the test configuration](image)
where: $I(f)$ – amplitude of current in the electromagnet coil at the frequency $f$, $\alpha(f)$ – amplitude of the PWM factor at the frequency $f$.

It appears that the maximal frequency of variations of the command signal is 188 Hz, which means that the minimal sampling frequency of the control algorithm should be equal to 376 Hz. The set-up for ACS testing is shown in Fig. 4.

Testing was done on the ACS system in the open loop configuration (ACS-O) and in the closed-loop configuration (ACS-PID). The applied control signal was current in the electromagnet coil. Electrical parameters of the electromagnet coil were: resistance $1.059 \, \Omega$, induction $1.13 \, mH$.

The PI version of the PID controller, used in the ACS-PID system, is governed by the equation (2).

$$u(t) = u_0 + K_p \varepsilon(t) + \frac{1}{T_i} \int_0^t \varepsilon(\tau) \, d\tau$$  

(2)

$$\varepsilon(t) = i_0 - i(t)$$  

(3)

where: $u(t)$ – control, $u_0$ – control value in the steady state, $\varepsilon(t)$ – control error, $i_0$ – preset current level in the coil, $K_p$ – controller gain, $T_i$ – integration time.

The parameters of the PI controller chosen using the parametric optimisation methods are: $K_p = 49.3; T_i = 1.74 \times 10^{-4}$. The integration function of the PI controller is approximated by the rectangle method.

<table>
<thead>
<tr>
<th>ACS</th>
<th>1 A</th>
<th>3 A</th>
<th>5 A</th>
</tr>
</thead>
<tbody>
<tr>
<td>ACS-O</td>
<td>1.92 ms</td>
<td>2.1 ms</td>
<td>1.96 ms</td>
</tr>
<tr>
<td>ACS-PID</td>
<td>1.32 ms</td>
<td>1.36 ms</td>
<td>1.16 ms</td>
</tr>
</tbody>
</table>

Tab. 2. Time required for reaching the preset current level in the control coil.

Testing of the ACS-O and ACS-PID system was done for the pre-defined PWM signal frequency (5 kHz) and the duty cycle $\alpha$ was varied from 0 to 1 with the step 0.1. As it was mentioned previously, the primary function of the ACS-PID system is to stabilise the current level in the electromagnet coil. This configuration of the system was tested for the preset current levels $i_0$: 1, 2, 3, 4, 5 A. The patterns of the PWM control and current in the coil are shown in Fig. 5.
registered for the ACS-O and ACS-PID configurations are shown in Fig. 5, for $i_0=1$ A, $i_0=3$ A and $i_0=5$ A. The current $i(t)$ plots show that the required current level $i_0$ was reached in the electromagnet coil, both in the ACS-O and the ACS-PID configuration. Application of the PI controller allowed a 30% reduction of the time needed to reach the preset current level (see Tab. 2). One has to bear in mind, however, that for smaller values of $i_0$ these time intervals can become decidedly shorter.

4. SUMMARY

This paper summarises the structure design of an MSP430-based ACS and laboratory test data are provided. The engineered ACS was tested in the laboratory conditions in two configurations: the open loop and in the feedback loop configuration (with a PID controller). Testing was done under the loading applied to the power interface VI-MR through an electromagnet coil. Experimental results show that application of the PI controller allowed a 30% reduction of the time needed to reach the preset current level and force produced by VI-MR in real application.

It is worthwhile to mention that the selected microcontroller MSP430 has a sufficient computational power to implement algorithms and allows for expanding the ACS structure such that it should incorporate extra sensors and actuators. ACS tests will be repeated once the final version of the vibration isolator VI-MR has been developed.

REFERENCES


The work has been accomplished under the research project No. PBS 1/A6/3/2012.